

NIELIT CALICUT

M.Tech 2024 Admission

Participants get exposure to different families and architectures of Embedded System, IoT, DSP and VLSI Designs

Participants will develop the expertise required to design electronic products, embedded system hardware and software using latest tools and become industry ready.

Participants will become highly proficient in Embedded/ Electronic System Design practices.

1 Electronics Design Technology

2 Embedded Systems



Affiliated to APJ Abdul Kalam Technological University.



Approved by AICTE.



NIELIT CALICUT

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ISO 9001-2015

National Institute of Electronics and Information Technology (NIELIT) is an Autonomous body of **Ministry of Electronics & Information Technology (MeitY), Government of India**. It is Premier Organization for Education, Training, R&D and Consultancy in Electronics & Information Technology.



Eligibility criteria

How to Apply

Total Seats

Important dates

Fees

Placements

Lab facilities

Contact details

Gallery

IMPORTANT INFORMATION

Important Dates	
Commencement of submission of online application	04-04-2024
Last Date for submission of online application	12-06-2024
Counselling /Admission/ Fee payment	24-06-2024
Reporting/ Commencement of Classes	Will be announced later
Important Telephone Numbers	
Office	0495 - 2287166
Director's Office	0495 - 2287123
Coordinator - M.Tech	0495 – 2287166 / 94460 13866, 99954 27802 (WhatsApp) 9447541518 (WhatsApp)
Email	mtech@calicut.nielit.in

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1. About us

National Institute of Electronics and Information Technology (NIELIT) is an autonomous body of Ministry of Electronics & Information Technology (MeitY), Government of India. It is a Premier Organization for Education, Training, R&D and Consultancy in Electronics & IT.

The history of NIELIT dates back to 1974 when the Department of Electronics (DoE) now Ministry of Electronics & Information Technology (MeitY), Government of India and the University Grants Commission (UGC) set up the first CEDT within the premises of Indian Institute of Science, Bangalore with assistance from Swiss Development Corporation. The objective was to bridge the gap between the academic institutions and industries. A decade after the successful running of CEDT, Bangalore, the then Department of Electronics (DoE), initiated a program to set up similar Centres in other parts of the country with a wider objective to develop human resources at different levels and in different specialized areas of Electronics Design. CEDT Calicut was established in 1989. Later in 2011, it was renamed as NIELIT Calicut.

The present infrastructure is developed in 25 Acre campus and houses state of the art laboratory facilities as well as hostels, guest house, Canteen, 24x7 Wi-Fi, ATM etc in lush green scenic ambience. The unique Kerala style architecture of the buildings and its proximity to world class institutions like NIT & IIM, makes it one of the best NIELIT Centres in the country.

The institute is engaged in the conduct of industry oriented quality education and training in the state-of-the-art areas through various formal and non-formal programs. The Centre is an implementing agency for various Government Schemes related to human resource development in the field of Information, Electronics & Communications Technology (IECT). The center is also engaged in R & D activities and provides product development and industrial consultancy services. NIELIT Calicut is also a Participating Institute under ISEA-II Chip to Start Up (C2S) and Drone/UAS Technology (SwaYaan) Projects of MeitY, Government of India

The center has 5 excellent laboratories, fully equipped with the latest systems and development tools in the area of **Embedded Systems, VLSI, Product Engineering, Information Technology, 3D Printing & Additive Manufacturing, Remote Hardware Prototyping Lab, Drones & Allied Technology and Process Control & Instrumentation**. Large collection of reference books as well as ebooks in the above areas are accessible to the students from the Centre's library in addition to IEEE, Elsevier/ScienceDirect online journal access & NKN connectivity. All the labs, library and office are connected through the Central network and students can retrieve information from their terminals itself and through well connected Wi-Fi system. The fully furnished NIELIT, Calicut hostels (ladies & men's) can accommodate around 200 students.

The Institute has highly qualified and best available talents in the fields, as its faculty who have undergone specialized training in various International Universities and Industries in USA, UK, Germany, Netherlands etc. with many years of experience in Embedded Systems. This includes post graduate and graduate engineers having ample teaching and industrial/R&D experience and highly skilled technical support staff to assist the faculty and students in their activities.

2. Our Objectives

- To train manpower in Electronics Product Design, Manufacturing, Maintenance and IT.
- To impart quality IT training through National Level Examinations and Certifications.
- To develop Entrepreneurs and Designers in Electronics and IT.
- To undertake Product Development, Contract Research, and Consultancy.
- To provide technical support to Industries in Application of Electronics and IT.

3. Our Vision

To be the leader in the development of Industry oriented quality education and training and be the country's premier Institution for examination and certification in the field of Information, Electronics and Communications Technology (IECT).

4. M.Tech Programs

Following two M Tech programs are offered at NIELIT Calicut, approved by AICTE and affiliated to APJ Abdul Kalam Technological University, Kerala

1. **Embedded Systems.**
2. **Electronics Design Technology.**

Above M.Tech programs are of two years duration having four semesters. These courses focus on practical design aspects of Embedded Systems and Electronic Product Design and are intended for students, who wish to work in the Industry as Design Engineers. The core and elective papers are practical oriented. This program is also suitable for those aspiring to research or take teaching as a profession in these areas.

To maximize the engineering, design and problem solving skills of the students and to become more adaptable to real life situations, project work in Industry is very helpful. This not only provides an opportunity to the students to have real life problem solving experience/ project work, but would also help them to apply their class room/ laboratory knowledge to live situations. To achieve this objective, students are given the option to do the project work at reputed industries.

5. Objective of the Course

To mold fresh electronics engineers and to upgrade working engineers to high caliber Embedded System Designers and Electronics Product Designers by enhancing their knowledge and skills in various hardware and software design aspects of Electronic Systems. These courses cover a range of topics of immediate relevance to industry and make the students suitable for working in industries engaged in Embedded System and Electronic Product development. These courses also provide an excellent foundation for those wishing to engage in application research in this rapidly developing area.

6. Outcome of the Course

- Participants get exposure to different families and architectures of Embedded System such as Microcontrollers, ARM Processors, DSPs, FPGAs, RTOS etc.
- Participants will develop the expertise required to design electronic products, embedded system hardware and software using the above tools and become industry ready.
- Participants become highly proficient in Embedded Software particularly in real-time programming

7. Affiliations & Approvals

M.Tech courses are affiliated to **APJ Abdul Kalam Technological University, Kerala.**

M.Tech courses are approved by AICTE *and approval for the year 2024-25 is under process.*

8. Distribution of Seats

M.Tech in Embedded Systems, Electronics Design Technology has 18 seats each. Reservations shall be as per the existing government rules. The seat distribution in both the M.Tech programs are as given below.

General (<i>including Physically handicapped &SEBC</i>)	10	SC/ST	3	Sponsored	5
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9. Eligibility

- a) The candidate shall be an Indian National
- b) The candidate should have passed the B.E/B.Tech course or equivalent from an institution approved by the All India Council for Technical Education (AICTE) /UGC approved Universities or Deemed Universities in India.
- c) Candidates should have a minimum of 60% aggregate marks in the engineering degree examination. For SEBC (OBC-NCL)/PWD candidates, a minimum of 54% (aggregate) marks in the Engineering Degree examination is mandatory. For SC/ST candidates, a pass in the engineering degree course is sufficient.
- d) Candidates having a **valid GATE Score** shall be given preference for admission.
- e) The eligible branches of engineering for each M.Tech are given under the respective program.

- I. Embedded Systems

- Electronics/ Electronics and Communication/ Electronics and Instrumentation
- Electrical and Electronics
- Applied Electronics and Instrumentation/ Instrumentation and Control
- Computer Science/ Computer Science and Engineering.
- Information Technology
- Biomedical Engineering / Electronics and Biomedical Engineering / Biomedical Instrumentation Engineering.

And **Valid GATE** score in Electronics/ Electrical/ Instrumentation/ Computer Science/ IT/ Biomedical engineering subjects.

- II. Electronics Design Technology

- Electronics and Communication Engineering.
- Electrical and Electronics Engineering
- Applied Electronics and Instrumentation Engineering/ Electronics and Instrumentation Engineering/Instrumentation and Control Engineering.
- Electronics and Biomedical Engineering.

And **valid GATE** score in Electronics/ Electrical/ Instrumentation/ Biomedical engineering subjects.

- f) However **Non GATE** candidates can also apply. They will be considered against the vacancies due to lack of candidates with valid GATE score. These unfilled GATE seats will be first filled with NON GATE candidates from Kerala State based on the nativity certificate. If seats are still remaining vacant, candidates from other states will be considered for admission.
- g) Candidates who have passed AMIE/AMIETE Examination in **Electronics** and satisfying the following conditions are also eligible for admission.
 - I. They must have a valid GATE score
 - II. A Minimum marks of 55% in section B in AMIE/AMIETE Examination
 - III. Minimum 2 years of professional experience in the field of specialization after acquiring the qualifying degree.
- h) Candidates who have **appeared/going to appear for the final semester examination** may also apply provided he/she has passed all the subjects up to and including the 6th semester examination. Such candidates may be considered for provisional admission only. Any candidate admitted provisionally as mentioned above shall have to discontinue the course, if he/she does not produce the final complete mark list and provisional/ degree certificate on or before **30.09.2024 or as per University guidelines.**

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10. Sponsored Seats [5 seats in each branch]

A sponsored candidate must have at least **2 years** of service on the date of application. Their employers must sponsor them and relieve them to pursue the program for the full duration of the course. GATE score is not mandatory for sponsored candidates.

Candidates from Central/State Govt. Institutions, Public Sector Organizations and reputed Private Organizations/industry will be considered in this category. Teachers sponsored from AICTE recognized Engineering Colleges will also be considered in this category.

11. Selection of Candidates

The admission to the course shall be based on the following criteria:

1. Admission shall normally be restricted to those with valid GATE score and seats shall be filled based on GATE Score merit. However, this stipulation is relaxed in the case of Sponsored candidates.
2. In case seats remain vacant due to lack of candidates with valid GATE score, candidates from Kerala State without valid GATE score will be considered against such vacancies. The selection of which shall be based on marks in the qualifying degree examination. They should produce nativity certificate. If seats are still remaining vacant, candidates from other States will be considered for admission based on marks in the qualifying degree examination.
3. In the case of students with the same GATE score then the selection shall be based on the marks in the Qualifying degree.
4. In case a tie still exists, the selection shall be based on the marks in the next lower education qualification.
5. For **Sponsored candidates**, minimum 2 years of experience on the date of application is required and they should produce the necessary certificates such as the Original sponsorship certificate (as per **Annexure 1**) and Original Experience Certificate at the time of counseling/admission. Selection of these candidates is based on marks in the qualifying examination and performance in the interview conducted by the selection committee.
6. Vacant Sponsored seats shall be filled with eligible candidates from the general list.
7. Candidates should produce conversion formula of their CGPA score if the same is not specifically stated in the mark list or certificate. Otherwise the conversion formula of APJ Abdul Kalam Technological University will be taken.
8. The date of counseling/admission **is listed in Important dates (Section 20)**. **All are required to attend the counseling and admission in person or virtual mode** on the day specified with the original mark lists, certificates and other necessary documents. **Individual call letter will not be send to the candidates**. Counseling will be done based on the selection list published on NIELIT Calicut website.
9. Admission is subject to satisfying the requirements and availability of seats. Attending the counseling does not guarantee admission.
10. Candidates offered admission will have to remit the one-year fees or at least one-semester fees on the same day of counseling, failing which the admission is not guaranteed.
11. **Transfer Certificate issued from the institution last attended must be produced at the time of admission.**

12. Course Fees

Fees shall be **Rs 65,000/-** per semester which includes the semester tuition fees of Rs 45,000/- and special laboratory tuition fees of Rs 20,000/-.

SC/ST students are eligible for full tuition fee waiver and **need to pay only Rs 8,500/-** (University Affiliation fee and Refundable caution deposit) at the time of admission.

The semester fee is excluding the hostel and food charges. Other university fees shall be extra for students of all categories. Semester exam fees shall be as decided by KTU from time to time and to be paid by students.

Description	Sem1	Sem2	Sem3	Sem4
Tuition & Lab fees	65,000/-	65,000/-	65,000/-	65,000/-
University Affiliation fees	3,500/-	Nil	Nil	Nil
<i>Refundable caution deposit</i>	5,000/-	Nil	Nil	Nil
Total	73,500/-	65,000/-	65,000/-	65,000/-

13. Application Processing Fee/ Payment Mode

Candidates can pay the application processing fee of Rs **1000.00** for general and **Rs 500.00** for SC/ST candidates through **online mode through this portal**. No other mode of payment shall be allowed.

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14. Additional Certificates

For candidates with qualifying degree from Universities and Deemed universities outside Kerala, additional certificates such as equivalence certificate, migration certificate etc. **may be produced, as and when required**.

15. Scholarship

All GATE qualified candidates are eligible for **Teaching Assistantship (TA)** subject to AICTE and MHRD guidelines. This facility will not be available to students admitted under Sponsored Candidates quota, Foreign Nationals and Dependents of NRIs. Disbursement of TA will only be done after the Govt. of India makes funds available. As per MHRD directives, a fellow holding TA shall not accept or hold any appointment paid or otherwise or receive any emoluments, salary, stipend etc from any source during the tenure of the award. The students joining the program under this category will be considered for Teaching Assistantships provided by AICTE, based on the following norms:

- (a) Students getting assistantship will be required to assist / work for courses, laboratory, or any other related academic / administrative work to the extent of 8 to 10 hours per week related to teaching and research activities as assigned by the institutions.
- (b) The students must secure first class or equivalent during the first Semester examination to become eligible for continuance of scholarship.
- (c) The student shall give an undertaking to the effect that he/she will not leave the course midway or appear for any competent Examination not related to Engineering and Technology.
- (d) The students shall not receive any emoluments, salary, stipend etc. from any other source during the course of study.
- (e) The assistantship will be available for a maximum period of 24 months and students with TA have to complete M.Tech in 2 years.
- (f) Assistantship will be paid on the basis of monthly attendance.

16. Discontinuing the Course & Refund of Fees

1. In the event of a student withdrawing **before start of the course and before closing date for admissions**, the refund of fees shall be the entire fee less Rs. 1000/-
2. In the event of student leave after joining the course and if the seat vacated by the student falling vacant has been filled by another student before the closing of admission, the fee collected will be returned with proportionate deductions of monthly fee and proportionate hostel fees if any. The course fee shall be Rs. 10833.00 per month (Semester fees of Rs. 65000.00 per semester spread over six months). The hostel fee shall be the yearly hostel fee spread over 12 months. The monthly fees and the hostel rent up to the month the student leaves shall be collected and the balance amount shall be returned.
3. If the student discontinues the course after the closing date of admissions, and the vacated seat is NOT filled by another applicant, satisfying eligibility conditions of APJ Abdul Kalam Kerala Technological University, the discontinuing student shall be refunded only the caution deposit on submission of “No Dues Certificate” after adjusting any dues payable by the student to the institution.
4. If any provisionally admitted student discontinues the course either due to non-satisfaction of eligibility conditions or non-production of eligibility documents etc. before the set due date (i.e 02 September 2024) shall be refunded only the caution deposit on submission of “No Dues Certificate” after adjusting any dues payable by the student to the institution.
5. If any student admitted for the course availing scholarship, stipend, fee concession etc. from any government organizations and discontinues the course after closing date of admission for the course, to join other course or for any other purpose, shall return all the scholarship, stipend, fee concession etc. to the authorities concerned.
6. Transfer certificates for the cases covered under the points 2, 3, 4 & 5 shall be issued on settlement of dues payable by the discontinuing student to the institute.

17. Ragging Cases

In case the applicant for admission is found to have indulged in ragging in the past his/her admission shall be refused or if it is noticed later that he/she has indulged in ragging in the past or is indulging in ragging during the period of the M.Tech course duration, the concerned student shall be given liberty to explain and if his/her explanation is not found satisfactory, his/her admission gets cancelled and the authority would expel the student from the institution. Further he/she has to pay a liquidated damage of Rs. 65,000.00. An affidavit has to be filled by the student and parent as per Government/ AICTE/ UGC guidelines.

18. How to Apply?

1. Candidates should apply through **on-line mode only**.

Application will be collected online only. **On-line application** link is available at - <https://www.calicut.nielit.in/mtech/MTechApplication.aspx>

2. The following documents should be uploaded along with the application:

- i. Copies of the Mark Lists & Degree Certificate of the Qualifying Examination. **OR**

Course Completion Certificate from the Head of the Institution last studied and attested copies of Mark Lists up to the pre-final semester/ pre-final year for those who have appeared in the qualifying examination and are awaiting results.

- ii. Copy of professional experience for AMIE/AMIETE (if applicable).
- iii. Copy of Nativity Certificate (if applicable) issued by the Village Officer/ Tahsildar or any competent authority of Kerala State.
- iv. Copy of proof of Date of Birth/SSLC or Equivalent.
- v. Copy of the GATE Score Card (if applicable).
- vi. Copy of Sponsorship Certificate (as per **Annexure 1**) and Experience Certificate in the case of **Sponsored Candidates**.
- vii. Copy of Community Certificate from a competent authority not below the rank of a Tahsildar (as per **Annexure 2**) in the case of **Scheduled Caste/Scheduled Tribe candidates**.
- viii. Copy of Community and Income certificate issued by the Village Officer in the case of **candidates who claim eligibility under SEBC**.
- ix. Physical disability certificate (if applicable).
- x. Transfer Certificate from the institution last studied.
- xi. Conduct Certificate from the institution last studied

3. **Candidates can apply for both the M.Tech programs using a single application with an application processing fee of Rs 500/- for SC/ST & PWD and Rs. 1000/- for others. Preferences of courses should be clearly mentioned in the application form.**

19. Counseling

Counselling/interview to admission will be done in **offline mode only**. All selected candidates have to report for counselling on the day of admission.

All the selected candidates (as per the selection list published in NIELIT Calicut website) have to be present for the counseling on **the admission date**. There shall not be any separate intimation for the counseling.

The selected candidates have to pay the full fees or one-semester fees for admission on the same day. Transfer Certificate in original issued from the institution last attended must be produced at the time of admission.

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20. Important Dates	
Commencement of submission of online application	04-04-2024
Last Date for submission of online application	12-06-2024
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Reporting/ Commencement of Classes	Will be announced later
21. Important Telephone Numbers	
Office	0495 - 2287166
Director's Office	0495 - 2287123
Coordinator - M.Tech	0495 – 2287266 / 9446013866, WhatsApp - 9995427802 / 9447541518
22. Address for Correspondence	
M.Tech Coordinator, NIELIT, PB # 5, NIT Campus P.O., Calicut – 673601, Kerala, India	
e-mail:	mtech@calicut.nielit.in
Website:	https://nielit.gov.in/calicut
[Home]	
23. Office Timings	
	The office timings are from 9.00 am to 5.30 pm with half an hour Lunch break at 1.00 pm. Saturday and Sunday are holidays.
24. Class & Lab Timings	
	This program is a practical oriented one; hence there shall be more lab than the theory classes. The classes and labs are from 9.30 am to 12.45 pm and 1.30 pm to 5.00 pm Monday to Friday.
25. Project Work	
	The project work of two semester duration is part of the course and can be done in an Industry or in NIELIT during the third and fourth semesters.
26. Placement	
	We have a placement cell, which provides placement assistance to students who qualify our courses. The Syllabus for this M.Tech is prepared and modified in collaboration with reputed Embedded System Companies in Bangalore and Techno Park Trivandrum such as Wipro, GE Power Controls, Robert Bosch, Philips, Texas Instruments, Analog Devices, Wind River, BBS, MindTree, TATA Elxsi, etc. The previous year's students are placed in companies such as TI, Analog Devices Intel, Qualcomm, AMD, Xilinx, Microchip, NXP Semiconductor, Continental Automotive, , Robert Bosch, Tata Elxsi, Valeo India, Unstring, QUEST Global, NeST SFO Technologies Mistral, Kyocera Wireless, National Instruments,

Schneider Electric, Honeywell, EnSilica, WIPRO, HCL Tech, TCS, Infosys, UST Global, Mindtree, Siemens, SiSoC Semiconductor Technologies Pvt Ltd, Avench Systems, DCT Digital Solutions, Einen Tech, VVDN, Procsys, Gadgeon, Ignitarium Technology Solutions, Jiva Sciences Pvt. Ltd, Mobilexion, Path Partner, Captronics, , Sankhya Technologies, Caterpillar, Test & Verification Solutions Pvt Ltd, Visteon Technical Services Centre, Westghats etc.

The students can do their final year project at Industries which indirectly helps them in getting a dream job in addition to the regular placement initiatives.

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27. Lab Facilities

The Centre is equipped with state-of-the art lab facilities in Embedded Systems in the country. These Include,

- VxWorks RTOS
- RTLinux RTOS
- EMI/EMC Test Setup, Logic Analyzer
- 32-bit ARM Cortex Microcontroller Development Systems – STM32 ARM Cortex-M3, M4, M7 and M33 Kits.
- Open Mote
- Beagle Bone Black, Beagle Bone Black Wireless and Rpi4 Kits with Accessories.
- Friendly ARM Kit with Accessories
- RPi Pico & PicoW Kits
- IoT development Kits developed by NIELIT Calicut
- ESP8266, ESP32 kits
- Sensors and actuators for interfacing with Microcontrollers
- GSM/GPRS/EDGE Modems.
- GPS receiver Modules
- Bluetooth/Zigbee Modems
- WiFi Aps and WiFi Modem
- LoRa & LoRaWAN Gateway
- Sub1GHz Modules
- Software Defined Radio Modems
- Intel Galileo Kits
- Arduino Uno Boards
- Test & Measuring Equipment's - Digital Storage & Mixed Signal Oscilloscopes
- FreeRTOS, Contiki OS
- Wireless Simulators expertise in Glomosim, NS2, NS3, etc.
- PC Based EDA tools (ORCAD)
- Matlab, Simulink, TI 'C6000 target for Matlab, LabView

The VLSI lab is furnished with the state-of-the art lab facilities and are given below:

Hardwares

- Ultrasound Research Platform (Verasonics USA)
- Intel (Altera) Development Boards & Trainer Kits
- Xilinx Development Boards & Trainer Kits(High End to Basic Kits)
- PSoC Development Boards
- Ultrasound Data Acquisition Systems & associated Test Boards
- In-House Fabricated FPGA Kit.
- Raspberry Pi IoT Kits

Testing Facilities

- Digital Storage & Mixed Signal Oscilloscopes
- Logic Analyzer & SMD Reworkstation

Softwares

- Xilinx Vivado Tool Suite
- MATLAB Tool Suite
- Intel Quartus II

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28. Hostel facilities

Accommodations are available for men and women. Three occupancy rent is Rs 1600/- per month, double occupancy rate is Rs 2400/- per month and Single occupancy rate is Rs 4800/- per month. **Canteen** facility is available in campus with Mess rates of around Rs 4500/- per month.

29. Other Facilities

- **NKN Connectivity**
- **24x 7 Wi-Fi Campus**
- **IEEE Online Access**
- **Science Direct Subscription**

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30. Location & How to Reach?

We are located 22 kms away from Calicut City and is one bus stop before the NITC (REC), Calicut. The bus stop is 'NIELIT' or 'Panthrandu' or 12th Mile Stop and the present bus fare is Rs 30.00 from city. Calicut (Kozhikode) is well connected by Air, Train and Buses.

Last date to submit the application is 12-06-2024 5:00 P.M.

SPONSORSHIP CERTIFICATE

(This should be typed on the letterhead of the Sponsoring Organization and enclosed with application for admission).

To,
THE DIRECTOR,
NIELIT, CALICUT,
PB No. # 5,
NIT CAMPUS. P.O,
CALICUT, KERALA – 673601

Sub : Sponsoring of an employee for M.Tech Program

We hereby Sponsor the candidature of Shri / Smt. / Kum _____
_____, an employee in our organization, for joining his / her
M.Tech. Program in Embedded Systems / Electronics Design Technology at your Institute as a
fulltime candidate.

We certify that he/she is an employee of our organization for _____ years from
_____. We shall fully relieve him/her of his/her duties in the organization during the
entire period of the M.Tech program, to enable him / her to devote full time to his / her studies in
the Institute.

Name and signature of the Sponsoring Authority

Designation of the Sponsoring Authority

Date:

Office Seal

Performa for Other Backward Class (Non-Creamy Layer) Certificate

(CERTIFICATE TO BE PRODUCED BY OTHER BACKWARD CLASSES APPLYING FOR ADMISSIONS TO CENTRAL EDUCATIONAL INSTITUTIONS (CEIs), UNDER THE GOVERNMENT OF INDIA)

This is to certify that Shri/Smt./Kum. _____
 Son/ Daughter of Shri/Smt. _____
 of Village/Town _____ District/Division _____ in the
 _____ State belongs to the _____ Community which is
 recognized as a backward class under:

(i)	Resolution No. 12011/68/93-BCC(C) dated 10/09/93 published in the Gazette of India Extraordinary Part I Section I No. 186 dated 13/09/93.
(ii)	Resolution No. 12011/9/94-BCC dated 19/10/94 published in the Gazette of India Extraordinary Part I Section I No. 163 dated 20/10/94.
(iii)	Resolution No. 12011/7/95-BCC dated 24/05/95 published in the Gazette of India Extraordinary Part I Section I No. 88 dated 25/05/95.
(iv)	Resolution No. 12011/96/94-BCC dated 9/03/96.
(v)	Resolution No. 12011/44/96-BCC dated 6/12/96 published in the Gazette of India Extraordinary Part I Section I No. 210 dated 11/12/96.
(vi)	Resolution No. 12011/13/97-BCC dated 03/12/97.
(vii)	Resolution No. 12011/99/94-BCC dated 11/12/97.
(viii)	Resolution No. 12011/68/98-BCC dated 27/10/99.
(ix)	Resolution No. 12011/88/98-BCC dated 6/12/99 published in the Gazette of India Extraordinary Part I Section I No. 270 dated 06/12/99.
(x)	Resolution No. 12011/36/99-BCC dated 04/04/2000 published in the Gazette of India Extraordinary Part I Section I No. 71 dated 04/04/2000.
(xi)	Resolution No. 12011/44/99-BCC dated 21/09/2000 published in the Gazette of India Extraordinary Part I Section I No. 210 dated 21/09/2000.
(xii)	Resolution No. 12017/9/2000-BCC dated 06/09/2001.
(xiii)	Resolution No. 12011/1/2001-BCC dated 19/06/2003.
(xiv)	Resolution No. 12011/4/2002-BCC dated 13/01/2004.
(xv)	Resolution No. 12011/9/2004-BCC dated 16/01/2006 published in the Gazette of India Extraordinary Part I Section I No. 210 dated 16/01/2006.

Shri/Smt./Kum. _____ and/or his family ordinarily reside(s) in the
 _____ District/Division of _____ State. This is also to certify
 that he/she does not belong to the persons/sections (Creamy Layer) mentioned in Column 3 of the Schedule to
 the Government of India, Department of Personnel & Training O.M. No. 36012/22/93-Estt.(SCT) dated 08/09/

93, modified vide OM No. 36033/3/2004 Estt.(Res.) dated 09/03/2004, or the latest notification of the Government of India.

This certificate is being issued based on the annual income / status of the parents / guardian of the applicant as on financial year ending March 31, 2024.

Dated:

District Magistrate/ Deputy Commissioner, etc.

Seal

NOTE:

(a) The term 'Ordinarily' used here will have the same meaning as in Section 20 of the Representation of the People Act, 1950.

(b) The authorities competent to issue Caste Certificates are indicated below:

(i) District Magistrate / Additional Magistrate / Collector / Deputy Commissioner / Additional Deputy Commissioner / Deputy Collector / 1st Class Stipendiary Magistrate / Sub-Divisional Magistrate / Taluka Magistrate / Executive Magistrate / Extra Assistant Commissioner (not below the rank of 1st Class Stipendiary Magistrate).

(ii) Chief Presidency Magistrate / Additional Chief Presidency Magistrate / Presidency Magistrate.

(iii) Revenue Officer not below the rank of Tehsildar and

(iv) Sub-Divisional Officer of the area where the candidate and / or his/her family resides.

The date of issue of OBC (NCL) certificate should be after 31 March, 2024



National Institute of Electronics and Information Technology, Calicut

Placement M.Tech 2021-23 Batch



ABHIJITH PM
THRISSUR, KERALA
Embedded Software Intern,
Valeo India Pvt. Ltd



ANCHITH P ANIL
WAYANAD, KERALA
Analog Validation Intern,
NXP Semiconductors,
Bangalore



ANJANA P
KOZHIKODE, KERALA
Verification Intern,
Microchip Technology
Chennai



ANURAG K V
KERALA
Embedded Engineer Intern
Ignitarium Technology Solutions
Kochi



MALLANGI ASWINI
Application Engineer (SEBU),
National Instruments,
Bangalore



BALAKRISHNAN S
VILLUPURAM, TAMILNADU
MPU 32 APPS
Microchip Technology, Chennai



MIDHUNA P K
KERALA
FPGA to Senior Engineer
Tata Elxsi, Trivandrum



DHANYA V
CALICUT, KERALA
Software Architecture
Continental Automotive Components
India Pvt Ltd, Bangalore



ANUPRIYA B
KERALA
Technical Student
NXP, Hyderabad



GAYATHRI NAIR
CALICUT, KERALA
Software Architecture
Continental Automotive Components
India Pvt Ltd, Bangalore



HARISH JUNUTULA
CALICUT, KERALA
RTL Design
Unistring Technologies
Pvt. Ltd.



SAHAD IBN M BASHEER
CALICUT, KERALA
Embedded Systems
Ignitarium Solutions Pvt. Ltd.
Kochi

Placement M.Tech 2021-23 Batch



HEMANT SHARMA
JAIPUR, RAJ
Verification
Microchip Technology
India Pvt. Ltd., Chennai



MANEESHA M
KERALA
Validation Engineer
NXP India, Pvt. Ltd.



MAYURA L VIJAYAN
KERALA
NXP India, Pvt. Ltd.



MITHUN JOSE
KOLLAM, KERALA
Software
NXP Semiconductors Pvt Ltd,
Bangalore



PADMA PRIYA
KERALA
Design Verification
AMD, Bangalore



RAVEEM GOUDA
KARWAR, KARNATAKA
Embedded Software Engineer
AMD India, Pvt. Ltd, Hyderabad



RINTUMOL SALU
KERALA
CAE
Microchip Technology,
Chennai



UJWALA GRANDHI
KAKINADA, ANDHRAPRADESH
RTL developer
Unistring Technologies Pvt. Ltd.



FATHIMA A S
KERALA
FPGA
Tata Elxsi, Trivandrum



PEDDINTI SAI MANOHAR
VISAKHAPATNAM
Microchip Technology, Chennai



MOHAMMED DANISH T C
KERALA
Software Architecture
Continental Automotive Components
India Pvt Ltd, Bangalore

Previous Batches



ADITYA H
M.Tech. in Embedded Systems
Intern at NXP Semiconductors

“Motivation and skill development along with academics make a course complete and that is what I got at NIELIT. It has truly developed me into a well-rounded person. NIELIT offers a fantastic academic environment, seminars, interdisciplinary initiatives, as well as well-equipped labs. The professors here are exemplary in their teaching styles. It not just provides us with various platforms to showcase our abilities and skill but also helps us to learn all soft skills that are highly required. We get trainings based on interview skills, communication skills, effective time management many more. The placement team provide us with much-needed exposure on corporate expectations. Choosing NIELIT helped me to get a good opportunity in NXP.”

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APARNA P G
M.Tech. in Electronics Design Technology
Intern at Intel

“Life at NIELIT Calicut has been breathtaking and enriching. The environment not only helped me in imbibing knowledge but also in developing my overall personality, fueling confidence in me. The amazing and highly qualified faculty help students in getting in-depth knowledge about all the subjects that are most needed to become a key person in a dynamic corporate scenario. More emphasis is given for practical than theory which enables students to have hands on experience with latest technology. Our placement coordinator also guide and encourages me at each step thereby helping me secure my internship at such a reputed company. I am thankful to NIELIT and all faculty in helping me to lay my first step in career.”

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Karthik Raghavan M
M.Tech. in Electronics Design Technology
Intern at Microchip Technology Inc.

“NIELIT Calicut has a great set of facilities which makes it best place to acquire education. Faculties of NIELIT are highly knowledgeable. They appreciate student efforts and encourages them to practically implement ideas. The institute provides good placement assistance too. Here we are taught based on industry demands and this not only gives us a glimpse of how the industry works, but also helps us in getting good jobs. NIELIT offers active learning atmosphere where students can easily connect through their studies. Its campus is located in a lush green environment and entire ambiance makes it more favourable for studies. I am thankful to NIELIT Calicut for providing me the opportunity to study here.”

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Gallery



Gallery



Campus



Hostel



Canteen



Nature club



Ladies hostel



Green campus

